

FORM PTO-1449 U.S. Department of
Commerce(Rev. 4/92) Patent and Trademark
Office

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

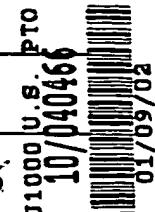
ATTY. DOCKET NO.
500.25179CP5

SERIAL NO.
Not assigned

APPLICANT
TANABE, et al

FILING DATE
January 9, 2002

GROUP 2666
Not assigned



U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TJ		4	5	6	9	0	4	1	2/86	Takeuchi et al	—	—	
JP		4	7	0	7	8	2	6	11/87	Froggatt	—	—	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT		
													YES	NO	
TJ	6	1	1	9	6	6	4	3	8/86	Japan	—	—		XX	
↑	6	0	2	5	3	3	9	6	12/85	Japan	↑	↑		XX	
	6	2	1	8	1	5	5		1/87	Japan				XX	
	6	1	3	5	6	4	8		2/86	Japan				XX	
	6	2	1	9	4	7	9	5	8/87	Japan				XX	
	6	2	8	2	7	4	7		4/87	Japan				XX	
	5	9	1	7	4	0	8	7	10/84	Japan				XX	
	6	0	7	6	4	2	9		4/85	Japan				XX	
	5	5	3	1	3	3	4		3/80	Japan				XX	
	6	1	3	5	6	4	6		2/86	Japan				XX	
	6	2	1	3	9	4	9	5	6/87	Japan				XX	
	5	7	1	0	3	4	8	9	6/82	Japan				XX	
↓	5	8	3	8	0	9	4		3/83	Japan				Eng. Abst.	
JP	6	1	1	8	7	4	9	7	8/86	Japan	—	—		XX	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER	DATE CONSIDERED
DANIEL J. DUNN	4/11/05

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		500.25179CP5	Not assigned
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANT TANABE, et al	
(Use several sheets if necessary)		FILING DATE January 9, 2002	GROUP Not assigned

YES	NO
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

T.D	H. Suzuki, et al, "A Study on the Configuration of the SCPS System", C&C Systems Research Labs, Integrated Switching Development Div. NEC Corporation, pp. 49-54.
↑	T. Takeuchi et al, "A New Switching System Architecture for ISDN Environment Synchronous Composite Packet Switching" Links for the Future, May 1984, pp. 38-41.
↓	H. Niwa et al, "Synchronous Composite Packet Switching for Broadband ISDN" C&C Systems Research Laboratories NEC Corporation, June 1986, pp. 1715-1719.
↓	H. Suzuki, et al, "Very High Speed and High Capacity Packet Switching for Broadband ISDN" C&C Systems Research Laboratories NEC Corporation, June 1986, pp. 749-754.
T.D	T. Yamaguchi, "Synchronous Composite Packet Switching", C&C Systems Research Laboratories, NEC Corporation.

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YES	NO
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

T:D	S. Morita, et al, "Elastic Basket Switching - A New Integrated Switching System for Voice and High-Speed Burst Data", IEEE, 1987, p. B711-3.
↑	T. Takeuchi et al, "Synchronous Composite Packet Switching for ISDN Switching System Architecture", ISS, May 7-11, 1984, Florence.
	A.K. Elhakeem, et al, "Analysis of a Hybrid (Demand Assignment IDMA) Protocol for Video Teleconferencing Voice Data Optical Networks", Computer Networks and ISDN Systems, March 1986, vol. 11, No. 3, pp. 219-241.
	R.W. Muisse et al, "Experiments in Wideband Packet Technology", International Zurich Seminar on Digital Communications, March 1986, Session D4, pp. 1-5.
	S. Gohara, et al, "A New Distributed Switching System Architecture for Media Integration" IEEE Int'l Conference on Communications Sound, June 7-10, 1987, Session, '11, paper 4, pp. 373-377.
	E. Gerretti and R. Melen, "An Experimental ATM Switching Architecture for the Evolving B-ISDN Scenario", May 27-June 1, 1990, pp. 15-20.
	S. Tanabe et al, "A New Distributed Switching System Architecture for B-IDSN", October 1990.
	K. Suzuki, "A Study on the Architecture of the ATM Switching Network", 1989, pp. 37-42.
	Y. Sakurai et al, "Asynchronous Transfer Mode Technology for Broadband ISDN", No. 29, 1990, Denshi Tokyo.
	Digital Switching Method, March 15, 1986.
	S. Takatsuka et al, "An Input Output Shared Buffer ATM Switch LSI, LSI Design Methodology with Higher Performance", B-462, 1991.
↓	An Experimental Synchronous Composite Packet Switching System, by Takeuchi et al, Swiss Federal Institute of Technology Proceedings, pp. 149-152, 3/86.
T:D	H. Yamanaka, "A High Speed ATM Switch Architecture Based on Multiple Shared Buffer Memories", B-467, Communication Systems Development Laboratory, Mitsubishi Electric Corporation.

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TJ		5	7	3	4	6	5	5	3/98	Sakurai et al	370	396	
JR		5	5	4	1	9	1	7	7/96	Farris	370	395	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT		
													YES	NO	
TJ		0	2	9	2	9	6	2	11/88	European	—	—			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

TJ	An Experimental Synchronous Composite Packet Switching System, by Takeuchi et al, Swiss Federal Institute of Technology Proceedings, pp. 149-152, 3/86.

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EXAMINER INITIAL	DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Ten	4	4	9	4	9	3	0		1/85	Turner	370	311	
↑	4	3	1	4	3	6	7		2/82	Bakka et al	370	351	
	4	4	8	6	8	7	8		12/84	Hauermans	370	60	
	4	4	9	1	9	4	5		1/85	Turner	370	60	
	4	5	1	6	2	3	8		5/85	Huang et al	370	60	
	4	4	9	4	2	3	0		1/85	Turner	370	60	
	4	6	7	9	1	9	0		7/87	Dias et al	370	60	
	4	7	8	5	4	4	6		11/88	Dias et al	370	60	
	4	9	2	2	4	8	7		5/90	Eilenberger et al	370	60	
↓	4	6	9	8	8	0	3		10/87	Haselton et al	370	60	
T.O	4	7	8	2	4	7	8		11/88	Day, Jr. et al	370	60	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT		
													YES	NO	
Ten	6	0	2	1	9	8	4	6	11/85	Japan	—	—	XX		
↑	6	0	2	3	2	7	4	2	11/85	Japan	↑	↑	XX		
	6	3	1	6	9	8	5	0	7/88	Japan	↓	↓	XX		
↓	6	3	3	0	5	6	4	3	12/88	Japan	↓	↓	XX		
T.O	3	1	4	3	0	9	8		6/91	Japan	—	—	XX		

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T.D	3	8	9	0	4	6	9	6/75	Kelly et al	370	60	
↑	4	5	5	8	4	4	4	12/85	Kennedy et al	370	63	
	4	6	7	0	8	7	1	6/87	Vaidya	370	60	
	4	7	5	4	4	5	1	6/88	Eng et al	370	60	
	5	2	1	4	6	4	0	5/93	Sakurai et al	370	60	
	4	4	8	6	8	7	5	12/84	Kelleher	369	170	
	5	0	4	3	9	7	9	8/91	Sakurai et al	370	60	
	4	6	3	3	4	6	0	12/88	Suzuki et al	370	581	
	4	6	0	3	4	1	6	7/86	Scivel et al	370	941	
JJ	4	9	4	7	3	8	8	8/90	Kuwahara et al	370	60	
T.D	4	9	1	0	7	3	1	3/90	Sakurai et al	370	60	

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	DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
												YES	NO
T.D	0	1	6	8	9	2	7	1/86	European	—	—		
T.D	0	2	8	9	7	3	3	11/88	European	—	—		
T.D	8	5	0	2	7	3	5	6/85	WO	—	—		

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EXAMINER D. AYNE, JDIV DATE CONSIDERED 4/11/05

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<i>T.O</i>	4	9	5	6	8	3	9	9/90	Torri et al	370	60	
	4	6	1	1	3	2	2	9/86	Larson et al	370	60	
	4	3	2	2	8	4	3	3/82	Beuscher et al	370	60	
	4	6	8	6	7	0	1	8/87	Ahmad et al	379	269	2/7/85
	4	3	4	5	3	2	4	8/82	Smitt	370	13	7/9/80
	4	1	5	2	5	4	8	5/79	Horiki	370	110.1	12/13/77
	4	0	0	9	3	4	7	2/77	Flemming et al	370	95.3	12/30/74
	4	6	8	5	1	0	4	8/87	Johnson et al	370	59	2/27/85
	4	6	4	4	5	2	9	2/87	Amstutz et al	370	60	8/2/85
	4	7	5	7	4	9	7	7/88	Delerle et al	370	85.12	12/3/86
	4	7	2	0	8	5	4	1/88	Sand	370	58.3	12/17/85
<i>T.O</i>	4	0	5	8	6	7	2	11/77	Crager et al	370	60	11/10/76

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT		
													YES	NO	
<i>T.O</i>		5	9	2	3	6	5	8	2/84	Japan	—	—	—		
<i>T.O</i>	6	0	1	2	7	8	4	4	7/85	Japan	—	—	—		
<i>T.O</i>		6	1	6	0	0	4	4	3/86	Japan	—	—	—		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER	<i>DMYC, TAN</i>	DATE CONSIDERED	<i>4/11/03</i>
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		4	0	5	8	8	3	8					
T-0	4 0 5 8 8 3 8	11/77	Crager et al	370	60								
↑	4 3 1 2 0 6 5	1/85	Ulug	370	94								11/10/76
	4 8 0 9 2 6 1	2/89	Ratcliff	370	60								7/10/87
	4 4 5 0 5 5 7	5/84	Munter	370	58								11/9/81
	4 8 6 6 7 0 2	9/89	Shimizu et al	370	54.3								9/21/86
	4 9 5 8 3 4 1	9/90	Hemmady et al	370	60.1								3/31/88
	5 5 1 3 1 7 7	4/96	Sakurai et al	370	58.1								2/13/91
	5 2 3 7 5 7 1	8/93	Cotton et al	370	60								
↓	4 7 6 0 5 7 0	7/88	Acampora et al	370	60								
RE T-0	3 1 1 8 2	3/83	Crager et al	370	60								
T-0	4 7 5 1 6 9 7	6/88	Hunter et al	370	68.1								9/5/86

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		YES	NO											
T-0	0 2 3 0 6 7 6	8/87	European	—	—									
↑	0 1 0 3 6 1 2	8/79	Japan	↑	↑									
	0 2 5 9 1 1 7	3/88	European											
↓	0 2 7 4 7 9 3	7/88	European	↓	↓									
T-0	0 3 4 3 6 1 1	11/89	European	—	—									

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DANG, DM

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DANIEL RON'

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